



PATENT AND TRADEMARK OFFICE  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: **Chung Zen Chen**

Examiner: **Huan Hoang**

Serial No.: **10/792,120**

Group Art Unit: **2827**

Filed: **March 3, 2004**

Confirmation No.: **3928**

For: **BIT SWITCH VOLTAGE DROP COMPENSATION DURING PROGRAMMING  
NONVOLATILE MEMORY**

**CERTIFICATION UNDER 37 C.F.R. §1.10**

I hereby certify that this document (and the documents referred to as attached therein) is being deposited with the United States Postal Service on the date shown below with sufficient postage as "Express Mail Post Office to Addressee" Mailing Label Number EV690215644US to the following:

Dated: 9/13/05

Joseph A. Powers  
Joseph A. Powers, Esq.  
(Reg. No. 47,006)

Mail Stop Amendments  
Commissioner for Patents  
Alexandria VA, 22313-1450

Sir:

**REPLY AND AMENDMENT UNDER 37 C.F.R. § 1.111**

**I. Introductory Comments**

The following remarks are responsive to the Official Action dated **June 14, 2005**.